

A RAMSEY THEOREM FOR BIASED GRAPHS

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ABSTRACT. A *biased graph* is a pair (G, \mathcal{B}) , where G is a graph and \mathcal{B} is a collection of ‘balanced’ circuits of G such that no Θ -subgraph of G contains precisely two balanced circuits. We prove a Ramsey-type theorem, showing that if (G, \mathcal{B}) is a biased graph which G is a very large complete graph, then G contains a large complete subgraph H such that the set of balanced cycles within H has one of three specific, highly symmetric structures, all of which can be described naturally via group-labellings.

1. INTRODUCTION

A Θ -*subgraph* of a graph G is a connected subgraph of G whose edge set is the union of three edge-disjoint xy -paths for some pair of distinct vertices x and y . Such a subgraph contains exactly three circuits of G . A *biased graph* is a pair (G, \mathcal{B}) where G is a graph and \mathcal{B} is a collection of circuits of G , called the ‘balanced’ circuits, so that no Θ -subgraph of G contains precisely two circuits in \mathcal{B} .

As was shown in Zaslavsky’s work [2] defining these objects, a natural class of biased graphs arises by assigning orientations and labels from a group to the edges of a graph G , and indeed, biased graphs arose as an abstraction of these ‘group-labelled graphs’. We explain the construction briefly here. For a group Γ , a Γ -*labelled graph* is a pair (\vec{G}, γ) , where \vec{G} is an orientation of an undirected graph $G = (V, E)$, (that is, G together with an assignment of a ‘head’ in $\{u, v\}$ to each undirected edge $uv \in E$, towards which the edge is ‘oriented’) and $\gamma: E \rightarrow \Gamma$ is a function.

For each circuit C of G with vertices v_1, \dots, v_k and edges e_1, \dots, e_k where v_i, v_{i+1} are the ends of e_i and $v_{k+1} = v_1$, let $\pi(C) = \prod_{i=1}^k \gamma'(e_i)$, where $\gamma'(e_i) = \gamma(e_i)$ if e_i is oriented from v_i to v_{i+1} , and $\gamma'(e_i) = \gamma(e_i)^{-1}$ otherwise. We say that C is *balanced* if $\pi(C)$ is the identity in Γ ; although $\pi(C)$ may depend on the particular choice of indexing of the vertices in C , associativity implies that the definition of balanced does not, even if Γ is nonabelian. Zaslavsky showed that (G, \mathcal{B}) is always a biased graph for the set \mathcal{B} of circuits of G that are balanced in the above sense. It is known, however, that not all biased graphs arise

from group-labelled graphs in this way; in fact, [1] shows that there exist infinite antichains (with respect to biased subgraph containment) of biased (multi-)graphs on three vertices that do not.

A *biased subgraph* of (G, \mathcal{B}) is a pair (H, \mathcal{B}') where H is a subgraph of G , and \mathcal{B}' is the intersection of \mathcal{B} with the set of circuits of H . We prove the following Ramsey-type theorem for biased graphs; K_n denotes the complete graph with vertex set $[n] = \{1, \dots, n\}$.

Theorem 1.1. *For every integer $s \geq 1$, there exists an integer n such that, if (G, \mathcal{B}) is a biased graph with $G \cong K_n$, then (G, \mathcal{B}) has a biased subgraph (H, \mathcal{B}') with $H \cong K_s$ that arises from a Γ -labelled graph for some finite cyclic group Γ .*

In fact, we refine the conclusion to show that (H, \mathcal{B}') arises from a graph with one of three very specific types of group-labelling, which we now define.

Let \vec{K}_n be the orientation of $K_n = ([n], E)$, where every edge with ends $i < j$ is oriented from i to j . Let $m = \binom{n}{2}$ and Γ be a cyclic group of order at least 2^m with generator g . Let $S \subseteq \Gamma$ be a set of size m such that no two distinct subsets of S have the same product; such a set S exists, as we can take $S = \{g^{2^i} : 0 \leq i < m\}$. Let $\gamma^u : E \rightarrow \Gamma$ be a labelling assigning distinct elements of S to each edge. Let $\gamma^o : E \rightarrow \Gamma$ be a labelling such that every edge receives a label in S , and two edges receive the same label if and only if they have the same head (that is, larger end).

For each integer $a \geq 0$, let Γ be a multiplicative cyclic group that is infinite if $a = 0$ and has size a otherwise, and let g be a generator for Γ . Let $\gamma^a : E \rightarrow \Gamma$ assign a label of g to every edge.

Let K_n^u, K_n^o, K_n^a be the biased graphs of the group-labelled graphs corresponding to \vec{K}_n and the labellings γ^u, γ^o and γ^a (for $a \geq 0$) respectively. While the first two ostensibly depend on the choice of Γ and S , Lemma 2.1 will show that in fact they do not. We can now state a refinement of our main theorem.

Theorem 1.2. *For every integer $s \geq 1$, there exists an integer n such that, if (G, \mathcal{B}) is a biased graph with $G \cong K_n$, then (G, \mathcal{B}) has a biased subgraph isomorphic to either K_s^u , K_s^o , or K_s^a for some $a > 0$.*

In all three cases, there are simple combinatorial descriptions for the set of balanced cycles in the subgraph. We state these here, and will prove them in Lemma 2.1. For a k -circuit C of K_n , let v_1, \dots, v_k be the unique ordering of $V(C)$ for which $E(C) = \{v_1v_2, \dots, v_{k-1}v_k, v_kv_1\}$, and $v_1 = \min(C)$ while $v_2 < v_k$. Set $v_0 = v_k$. For $i \in [k]$, we say that

the edge $v_{i-1}v_i$ is *positive* in C if $v_i > v_{i-1}$, and *negative* otherwise. We will show that

- K_s^u has no balanced circuits,
- a circuit C is balanced in K_s^o if and only if the edges of C alternate between positive and negative, and
- a circuit C is balanced in K_s^a if and only if $a|d$, where d is the difference between the number of positive edges in C and the number of negative edges in C .

Note that if $a = 1$ in the third case, then every circuit is balanced, and if $a = 2$ then precisely the even circuits are balanced; these natural cases are therefore both possible outcomes of Theorem 1.2.

The function $n = n(s)$ implicitly defined in Theorem 1.2 depends on Ramsey numbers for 4-colouring the edges of a complete 4-uniform hypergraph, and therefore corresponds to a tower of exponents of constant height. One could shorten our proof in exchange for much worse numbers (invoking Ramsey's theorem for $f(s)$ -uniform hypergraphs with $g(s)$ colours, where f and g are respectively linear and exponential in s). We also prove a bipartite version of our main theorem. Here the outcomes simplify and our bound is small enough to include explicitly.

Theorem 1.3. *Let $t \geq 1$. If $n \geq 2^{2^{4t}}$ and (G, \mathcal{B}) is a biased graph with $G \cong K_{n,n}$, then (G, \mathcal{B}) has a biased subgraph (H, \mathcal{B}') with $H \cong K_{t,t}$ such that \mathcal{B}' is either empty, or the set of all circuits in H .*

As part of the work we do to get our better bound in Theorem 1.2, we obtain a result of independent interest, Lemma 5.2, that gives a nearly complete characterisation of any biased complete graph in which only certain 4-circuits are required to be balanced. We state a slightly simplified version here. For a biased graph (G, \mathcal{B}) and a vertex v of G , we write $(G, \mathcal{B}) - v$ for the biased subgraph (H, \mathcal{B}') with $H = G - v$.

Theorem 1.4. *Let $n \geq 5$ and (G, \mathcal{B}) be a biased graph with $G = K_n$. If, for all $v_1, v_2, v_3, v_4 \in V(G)$ for which either $v_1 < v_2 < v_4 < v_3$ or $v_1 < v_3 < v_2 < v_4$, the circuit with edge set $\{v_1v_2, v_2v_3, v_3v_4, v_4v_1\}$ is balanced, then $(G, \mathcal{B}) - n = K_{n-1}^a$ for some $a > 0$.*

The single vertex deletion turns out to be necessary; in fact, Theorem 5.4 will show that if the statement is strengthened to assert that (G, \mathcal{B}) itself has the form K_n^a , then there are doubly exponentially many counterexamples.

2. ORDERED BIASED GRAPHS

Our biased graphs hereon are always complete. While we could phrase all our results in terms of graphs K_n with vertex set $\{1, \dots, n\}$,

when passing to subgraphs it is more convenient to state them in terms of biased graphs with a fixed linear ordering on the vertex set; this will also allow us to state a slightly stronger, ‘ordered’ version of our main theorem. An *ordered graph* is a pair (G, \leq) where G is a graph and \leq is a total ordering on $V(G)$, and an *ordered biased graph* is a triple (G, \mathcal{B}, \leq) , where (G, \mathcal{B}) is a biased graph and (G, \leq) is an ordered graph. Two ordered biased graphs $(G_1, \mathcal{B}_1, \leq_1)$ and $(G_2, \mathcal{B}_2, \leq_2)$ are *isomorphic* if there is a graph isomorphism from G_1 to G_2 that preserves both the property of being balanced for a circuit, and the ordering of the vertices. We defined our special biased graphs $K^u(n)$, $K^o(n)$ and $K^a(n)$ to each have a fixed vertex set $[n]$; from here on we view them as ordered biased graphs with the usual ordering on $[n]$.

We write either $[v_1, \dots, v_k]$, or $[v_i : i \in \mathbb{Z}_k]$ for the circuit with edge set $\{v_k v_1, v_1 v_2, \dots, v_{k-1} v_k\}$. Each k -circuit has $2k$ such representations. Given a circuit C in an ordered graph (G, \leq) , define the *canonical ordering* of C to be the unique sequence v_1, \dots, v_k for which $C = [v_1, \dots, v_k]$ and $v_1 = \min(V(C))$ while $v_2 < v_k$; this distinguished ordering was considered but not named in the introduction. We say that two k -circuits C, C' are *similar* in (G, \leq) if their canonical orderings v_1, \dots, v_k and v'_1, \dots, v'_k are such that $v_i < v_j$ if and only if $v'_i < v'_j$. This is an equivalence relation on the set of all k -circuits of G , and for each k -element subset X of G , each equivalence class contains precisely one circuit with vertex set X .

We will be especially concerned with two special similarity classes of 4-circuits. Call a 4-circuit C a *1324-circuit* if its canonical ordering v_1, v_2, v_3, v_4 satisfies $v_1 < v_3 < v_2 < v_4$ and define a *1243-circuit* similarly. Note that if $V = [n]$ with $n \geq 4$, then $[1, 3, 2, 4]$ and $[1, 2, 4, 3]$ are natural examples of these two types of circuit.

A circuit $[v_i : i \in \mathbb{Z}_k]$ is *oscillating* if for each $i \in \mathbb{Z}_k$, the vertex v_i satisfies either $v_i < \min(v_{i-1}, v_{i+1})$ or $v_i > \max(v_{i-1}, v_{i+1})$. This clearly does not depend on the choice of indexing, and is equivalent to the definition earlier that the edges of C alternate between ‘positive’ and ‘negative’. Oscillating 4-circuits are precisely the 1243-circuits.

For each circuit $C = [v_i : i \in \mathbb{Z}_k]$, let $\delta(C) = |\delta_0(C)|$, where

$$\delta_0(C) = |\{i \in \mathbb{Z}_k : v_i > v_{i-1}\}| - |\{i \in \mathbb{Z}_k : v_i < v_{i-1}\}|.$$

Cyclically permuting the v_i does not change either term in δ_0 , while reversing the order of the v_i swaps the two terms, changing only the sign of δ_0 ; thus, the parameter $\delta(C)$ is a well-defined function of C itself, not on the given indexing of its vertices. Fixing the ordering of $V(C)$ to be the canonical one, we see that the value of δ is the absolute difference between the number of ‘positive’ and ‘negative’ edges in C as defined

in the introduction. Oscillating circuits clearly satisfy $\delta = 0$, since the two sets defining δ_0 have equal size. Note that $\delta(C) \leq |V(C)| - 2$ and that $\delta(C)$ and $|V(C)|$ have the same parity.

Given these definitions, we can now prove the combinatorial characterisations of the special biased graphs K_n^u , K_n^o and K_n^a that were stated earlier. Recall that they all arise from group-labellings of the orientation \vec{K}_n of K_n in which edges are oriented ‘upwards’; the definitions of the relevant labellings are recalled in the proof below.

Lemma 2.1. *If $n \geq 1$ is an integer, then*

- (i) *The set of balanced circuits of $K^u(n)$ is empty,*
- (ii) *The balanced circuits of $K^o(n)$ are precisely the oscillating circuits of (K_n, \leq) , and*
- (iii) *if $a \geq 0$ is an integer, then the balanced circuits of $K^a(n)$ are precisely the circuits C of (K_n, \leq) for which $a|\delta(C)$.*

Proof. Let $G = (V, E) \cong K_n$. Let Γ be a cyclic group and $S \subseteq \Gamma$ be an $\binom{n}{2}$ -element set whose subsets all have distinct products.

Let $\gamma = \gamma^u: E \rightarrow \Gamma$ be a function assigning distinct elements of S to each edge, so K_n^u arises from (\vec{K}_n, γ^u) . Let $C = [v_1, \dots, v_k]$ be a cycle of K_n . Then $\pi(C) = \prod_{i \in \mathbb{Z}_k} (\gamma^u(v_i v_{i+1}))^{-b_i}$ where each b_i is -1 or 1 . So $\pi(C)$ is the identity if and only if $\prod_{i: b_i=1} \gamma^u(v_i v_{i+1}) = \prod_{i: b_i=-1} \gamma^u(v_i v_{i+1})$. The two sides of this equality are products of distinct subsets of S , so this cannot occur; thus, all circuits of $K^u(n)$ are unbalanced, as required.

Now let $\gamma = \gamma^o: E \rightarrow \Gamma$ be a function assigning an element of S to each edge, such that two edges receive the same label if and only if their larger end (head) is the same, so K_n^o arises from (\vec{K}_n, γ^o) . For each $v \in [n]$, let $s_v \in S$ be the label assigned to the edges whose head is v . Let $C = [v_i: i \in \mathbb{Z}_k]$. Since each edge is oriented from its minimum to its maximum, we have

$$\begin{aligned} \pi(C) &= \prod_{i \in \mathbb{Z}_k: v_{i+1} > v_i} \gamma^o(v_i v_{i+1}) \prod_{i \in \mathbb{Z}_k: v_{i+1} < v_i} \gamma^o(v_i v_{i+1})^{-1} \\ &= \left(\prod_{i \in \mathbb{Z}_k: v_{i+1} > v_i} \gamma_{i+1} \right) \left(\prod_{i \in \mathbb{Z}_k: v_{i+1} < v_i} \gamma_i \right)^{-1}. \end{aligned}$$

Note that since v_1, \dots, v_k are distinct, each of the two terms is a product of distinct elements of S . Therefore, by the choice of S , the circuit C is balanced if and only if $\{i \in \mathbb{Z}_k: v_{i+1} > v_i\} = \{i \in \mathbb{Z}_k: v_{i+1} < v_i\}$.

This occurs precisely when each v_i either satisfies $v_i > \min(v_{i-1}, v_{i+1})$ or $v_i > \max(v_{i-1}, v_{i+1})$: that is, when C is oscillating. This gives (ii).

Finally, let $a \geq 0$ be an integer and let Γ_a be a cyclic group with generator g such that Γ is infinite if $a = 0$, and $|\Gamma|$ has order a otherwise. Let $\gamma^a: E \rightarrow \Gamma_a$ be the labelling assigning g to every edge, so K_n^a arises from (\vec{K}_n, γ^a) . Then, for a cycle $C = [v_i: i \in \mathbb{Z}_k]$, we have

$$\begin{aligned} \pi(C) &= \prod_{i \in \mathbb{Z}_k: v_{i+1} > v_i} \gamma^a(v_i v_{i+1}) \prod_{i \in \mathbb{Z}_k: v_{i+1} < v_i} \gamma^a(v_i v_{i+1})^{-1} \\ &= g^{|\{i \in \mathbb{Z}_k: v_{i+1} > v_i\}| - |\{i \in \mathbb{Z}_k: v_{i+1} < v_i\}|} = g^{\delta(C)}. \end{aligned}$$

Since $g^{\delta(C)}$ is the identity if and only if $a|\delta(C)|$, the characterisation (iii) follows. \square

We also prove an easy folklore estimate for the number of (unordered) paths and circuits in a complete graph. This only serves to make the bounds in our final theorem slightly cleaner.

Lemma 2.2. *K_n has at most $2n!$ paths and at most $2(n-1)!$ circuits.*

Proof. Let p_n, c_n denote the number of paths and circuits respectively in K_n . Using $4 - e > 1$, we have $p_n = n + \sum_{i=2}^n \binom{n}{i} \frac{i!}{2} < \frac{1}{2}n + \frac{e}{2}n! < 2n!$. That $c_n \leq 2(n-1)!$ is easy to verify for $n \leq 4$; suppose that $n \geq 5$. Each circuit of K_n that is not a circuit of K_{n-1} corresponds to a nonempty path of K_{n-1} , together with vertex n added, so $c_n \leq p_{n-1} + c_{n-1}$ for each $n \geq 4$. Now, we inductively get $c_n \leq c_{n-1} + p_{n-1} \leq 2(n-2)! + \frac{e}{2}(n-1)! < 2(n-1)!$ (where we use $n-1 > \frac{4}{4-e}$), and the bound follows. \square

3. BIPARTITE GRAPHS

In this section and what follows, we use the following statement of Ramsey's theorem, as well as the very well-known bound $R_2(t, t) \leq 2^{2^t}$.

Theorem 3.1 (Ramsey's Theorem). *There is a function $R_k(n_1, \dots, n_\ell)$ so that, if Z is a finite set with $R_k(n_1, \dots, n_\ell)$ and each element of $\binom{Z}{k}$ is assigned a colour from $\{c_1, \dots, c_\ell\}$, then there is some $i \in [\ell]$ and a set $X \subseteq Z$ for which $|X| = k_i$ and each set in $\binom{X}{k}$ receives colour c_i .*

Call a set of circuits \mathcal{C} in a biased graph (G, \mathcal{B}) *consistent* if either $\mathcal{C} \subseteq \mathcal{B}$ or $\mathcal{C} \cap \mathcal{B} = \emptyset$. Call a biased graph (G, \mathcal{B}) *consistent* if the set of all its circuits is consistent. For a set $X \subseteq V(G)$, write $(G, \mathcal{B})|X$ for the biased subgraph (H, \mathcal{B}') where H is the subgraph of G induced by X . We now restate and prove Theorem 1.3.

Theorem 3.2. *Let $t \geq 1$ be an integer. If $n \geq 2^{2^{4t}}$ and (G, \mathcal{B}) is a biased graph with $G \cong K_{n,n}$, then (G, \mathcal{B}) has a consistent $K_{t,t}$ -subgraph.*

Proof. Let $n_0 = 4^t$; note that since $\binom{n_0}{2} < 2^{4t-1}$, we have $n^{2^{-\binom{n_0}{2}}} > 2^{2^t} > t + 2(2t-1)!$. Let (A, B) be a bipartition of G . Let A_0 be an n_0 -element subset of A . Let $X_1, X_2, \dots, X_{\binom{n_0}{2}}$ be an enumeration of $\binom{A_0}{2}$. Let k be maximal so that $0 \leq k \leq \binom{n_0}{2}$ and there is a set $B_k \subseteq B$ with $|B_k| \geq n^{2^{-k}}$ such that $(G, \mathcal{B})|(X_i \cup B_k)$ is consistent for each $1 \leq i \leq k$.

Suppose that $k < \binom{n_0}{2}$. Let H be a graph with vertex set B_k in which vertices u, v are adjacent if and only if the 4-circuit C_{uv} with vertex set $\{u, v\} \cup X_{k+1}$ is balanced. Since the 4-circuits C_{uv}, C_{uw} and C_{vw} form a Θ -graph for all distinct u, v, w , adjacency in H is an equivalence relation, so there is a set $B_{k+1} \subseteq B_k$ with $|B_{k+1}| \geq \sqrt{|B_k|} \geq n^{2^{-(k+1)}}$ that either contains no edges of H , or induces a complete subgraph of H . Now $(G, \mathcal{B})|(X_i \cup B_{k+1})$ is consistent for each $1 \leq i \leq k+1$, so the existence of B_{k+1} contradicts the maximality of k .

We may therefore assume that $k = \binom{n_0}{2}$; let $B' = B_k$, so $|B'| \geq n^{2^{-k}} > t + 2(2t-1)!$ as noted earlier. Now $(G, \mathcal{B})|(X \cup B')$ is consistent for each $X \in \binom{A_0}{2}$; since $n_0 = 4^t \geq R_2(t, t)$, there is a t -element set $A' \subset A_0$ such that the set of 4-circuits of $(G, \mathcal{B})|(A' \cup B')$ is consistent.

Let $B'' \subseteq B'$ be maximal so that every circuit in $G|(A' \cup B'')$ is unbalanced. We may assume that $|B''| < t$, as otherwise $G|(A' \cup B'')$ has a consistent $K_{t,t}$ -subgraph; thus $|A' \cup B''| \leq 2t-1$. By maximality, each $b \in B' - B''$ is in some balanced circuit $[b, x_1, \dots, x_\ell]$ where x_1, \dots, x_ℓ is a path. There are at most $2(2t-1)! < |B' - B''|$ such paths, so there exist $b, b' \in B' - B''$ and $x_1, \dots, x_\ell \in A' \cup B''$ for which $[b, x_1, \dots, x_\ell] \in \mathcal{B}$ and $[b', x_1, \dots, x_\ell] \in \mathcal{B}$. By the Θ -property we have $[b, x_1, b', x_\ell] \in \mathcal{B}$; since the set of 4-circuits of $G|(A' \cup B')$ is consistent, it follows that every 4-circuit of $G|(A' \cup B')$ is balanced.

We now argue that every circuit of $G|(A' \cup B')$ is balanced; since $|B'| \geq |A'| = t$, this implies the result. Indeed, a minimum-length unbalanced circuit $C = [c_1, \dots, c_{2k}]$ must have length greater than 4, but then $[c_1, c_4, c_5, \dots, c_k]$ and $[c_1, c_2, c_3, c_4]$ are smaller (and thus balanced) circuits forming a theta with C , a contradiction. \square

4. CLIQUES

We first prove a Ramsey-type result that will essentially find one of our three unavoidable examples in a large biased clique; however, recognising that the final outcome is (almost) K_t^a for some a will require a lemma from the next section. The proof of the following lemma uses the estimates from Lemma 2.2.

Lemma 4.1. *Let $r, s, t \geq 1$ and $n \geq R_4(3r!, 3s!, 5, t)$. If (G, \mathcal{B}, \leq) is an ordered biased graph with $G \cong K_n$, then either*

- *G contains a K_r -subgraph in which no circuit is balanced,*
- *G contains a K_s -subgraph in which the balanced circuits are precisely the oscillating circuits, or*
- *G contains a K_t -subgraph in which all 1243-circuits and 1324-circuits are balanced.*

Proof. Suppose for a contradiction that G has none of the above subgraphs. Let $(m_{\mathbf{b},\mathbf{b}}, m_{\mathbf{b},\mathbf{u}}, m_{\mathbf{u},\mathbf{b}}, m_{\mathbf{u},\mathbf{u}}) = (3r!, 3s!, 5, t)$. For each $Q \in \binom{V}{4}$, let $c(Q) = (\alpha, \beta)$, where $\alpha = \mathbf{b}$ if the unique 1423-circuit on X is balanced, and $\alpha = \mathbf{u}$ otherwise, and $\beta \in \{\mathbf{b}, \mathbf{u}\}$ is defined similarly for the unique 1243-circuit on X . By the definition of n , there is a subgraph H of G and some $(\alpha_0, \beta_0) \in \{\mathbf{b}, \mathbf{u}\}^2$ such that $c(Q) = (\alpha_0, \beta_0)$ for all $Q \in \binom{V(H)}{4}$, and $|V(H)| = m_{\alpha_0, \beta_0}$. Let $U = V(H)$.

4.1.1. *If $\beta_0 = \mathbf{b}$ then $\alpha_0 = \mathbf{b}$.*

Proof. Suppose not; so $(\alpha_0, \beta_0) = (\mathbf{u}, \mathbf{b})$ and $|U| = 5$; let v_1, \dots, v_5 be the vertices of U in increasing order. The Θ -graph containing circuits $[v_1, v_2, v_4, v_3], [v_1, v_2, v_5, v_3]$ and $[v_2, v_4, v_3, v_5]$ contains two (balanced) 1243-circuits and one (unbalanced) 1324-circuit, a contradiction. \square

4.1.2. $\alpha_0 = \mathbf{b}$.

Proof. If not, then $(\alpha_0, \beta_0) = (\mathbf{u}, \mathbf{u})$, so $|U| = 3r!$. Let $X \subseteq U$ be maximal such that X contains no balanced circuits. If $|X| \geq r$ then G has a K_r -subgraph with no balanced circuits, a contradiction; thus $|X| \leq r - 1$, so $U - X$ has a partition into at most r intervals, and $|U - X| > 3r! - r > 2r(r - 1)!$. By maximality, for each $v \in U - X$ there is a balanced circuit $C(v)$ contained in $X \cup \{v\}$. For each such v , the graph $P(v) = C(v) - v$ is a path contained in X , and there are at most $2(r - 1)!$ such paths; by a majority argument there exist $v_1, v_2 \in U - X$ for which $P(v_1) = P(v_2)$, and v_1 and v_2 lie in the same interval of $U - X$. Now $C(v_1)$ and $C(v_2)$ form a Θ -subgraph with a 4-circuit $C = [u, v_1, w, v_2]$ where u, w are the ends of $P(v_1)$, so C is balanced. Since u and w do not lie in the interval between v_1 and v_2 , C is either a 1324-circuit or a 1243-circuit. Thus either $\alpha_0 = \mathbf{b}$ or $\beta_0 = \mathbf{b}$, and the claim follows from 4.1.1. \square

The above claim gives that all 1324-circuits in H are balanced.

4.1.3. *All oscillating circuits in H are balanced.*

Proof. Suppose otherwise, and let C be an unbalanced oscillating circuit with t as small as possible, with canonical ordering v_1, \dots, v_t . Note

that $v_1 < v_3 < \min(v_2, v_4)$ and t is even. Clearly $t > 4$, since all oscillating circuits of length at most 4 are 1324-circuits. The circuit $C' = [v_1, v_4, v_5 \dots, v_t]$ is also oscillating, and is balanced by minimality. Now C and C' are in a Θ -graph with the (balanced) 1324-circuit $[v_1, v_2, v_3, v_4]$, contradicting the choice of C as unbalanced. \square

4.1.4. $\beta_0 = \mathbf{b}$.

Proof. Suppose otherwise, so $(\alpha_0, \beta_0) = (\mathbf{b}, \mathbf{u})$ (i.e. all 1243-circuits of H are unbalanced) and $|U| = 3s!$. Call a circuit *bad* if it is balanced but not oscillating. Let $n_1 = 2(s-1)!$, and let U_0, U_1, \dots, U_s be sets of vertices of H such that $|U_0| = s$, while for each $0 < i \leq s$ we have $|U_i| = n_1$, and the vertices of U_{i-1} precede the vertices of U_i in the ordering; these sets exist because $s + sn_1 < s + 2s! < |U|$.

Let u_1, \dots, u_s be the vertices of U_0 listed in increasing order. Let \mathcal{C} denote the set of circuits of H whose vertex set is contained in U_0 (noting that $|\mathcal{C}| < n_1$), and let $\mathcal{W} = U_1 \times U_2 \times \dots \times U_s$. For each $1 \leq i \leq s$ and $W = (w_1, \dots, w_s) \in \mathcal{W}$, let $\varphi_W(u_s) = w_s$. For each $C \in \mathcal{C}$, let $\varphi_W(C)$ denote the circuit obtained from C by applying the map φ_W to each vertex.

Note that the map $C \mapsto \varphi_W(C)$ is a bijection between \mathcal{C} and the set of circuits with vertex set contained in W , and that this map preserves the property of being oscillating.

Let $W = (w_1, \dots, w_s) \in \mathcal{W}$ be such that the number of bad circuits with vertex set contained in W is as small as possible. If this number is zero, then W is the vertex set of a K_s -subgraph in which precisely the oscillating circuits are balanced, a contradiction. Thus, there is some $C_0 \in \mathcal{C}$ for which $\varphi_W(C_0)$ is bad, so, since C_0 is not oscillating, there are consecutive vertices x, y, z in C_0 with $x < y < z$. Let A be the set U_i containing $\varphi_W(y)$, and for each $a \in A$, let W_a be obtained from W by replacing $\varphi_W(y)$ with a . Note that if $C \in \mathcal{C}$, then either

- (1) $y \notin V(C)$ and $\varphi_{W_a}(C) = \varphi_{W_{a'}}(C)$ for all $a, a' \in A$,
- (2) y is either the minimum or maximum of its two neighbours in C and, for all $a, a' \in A$, the circuits $\varphi_{W_a}(C)$ and $\varphi_{W_{a'}}(C)$ form a Θ -graph with a 1324-circuit (which is balanced by (4.1.2)), or
- (3) y lies between its two neighbours in C and, for all $a, a' \in A$, the circuits $\varphi_{W_a}(C)$ and $\varphi_{W_{a'}}(C)$ form a Θ -graph with an 1243-circuit (which is unbalanced by assumption).

For each $C \in \mathcal{C}$, consider the set A_C of elements $a \in A$ for which $\varphi_{W_a}(C)$ is balanced and $\varphi_W(C)$ is unbalanced. Note that if (1) holds then $A_C = \emptyset$. If (2) holds, then for each $a \in A_C$, the Θ -graph containing some 1324-circuit and the circuits $\varphi_W(C)$ and $\varphi_{W_a}(C)$ contains

exactly two balanced circuits, so $A_C = \emptyset$. If (3) holds and $a, a' \in A_C$ are distinct, then the Θ -graph containing $\varphi_{W_a}(C)$, $\varphi_{W_{a'}}(C)$, and some 1243-circuit contains exactly two balanced circuits, so $|A_C| \leq 1$.

Since $|\mathcal{C}| < n_1 = |A|$, there is some $b \in A$ that is outside every A_C . Note that $\varphi_W(C_0)$ is balanced and that, since y lies (in the ordering) between its two neighbours in C_0 , the circuit C_0 satisfies (3) and, since 1243-circuits are unbalanced, so is the circuit $\varphi_{W_b}(C_0)$. So $\varphi_{W_b}(C_0)$ is not bad. Moreover, by choice of b , there is no $C \in \mathcal{C}$ for which $\varphi_W(C)$ is not bad and $\varphi_{W_b}(C)$ is bad. Therefore W_b contains strictly fewer bad circuits than W , contradicting the choice of W . \square

By these claims, we have $(\alpha_0, \beta_0) = (\mathbf{b}, \mathbf{b})$ and $|U| = t$; thus, H satisfies the third outcome, a contradiction. \square

5. BALANCED 4-CIRCUITS

Let (G, \leq) be an ordered graph where G is complete with vertex set V . Define $\Omega(G)$ to be the graph whose vertices are the *nonspanning* circuits of G , in which two circuits C_1, C_2 are adjacent if and only if there is a Θ -subgraph of G containing C_1, C_2 and some 1243-circuit or 1324-circuit. Our first lemma describes the components of Ω .

Lemma 5.1. *Let $n \geq 5$ and let (G, \leq) be an ordered graph where $G \cong K_n$. Then two vertices F_1, F_2 of $\Omega(G)$ are in the same component of Ω if and only if $\delta(F_1) = \delta(F_2)$.*

Proof. We may assume that $G = K_n$ and \leq is the usual ordering. Let $\Omega = \Omega(G)$. Say that two vertices of Ω are *connected* if they are in the same component of Ω .

5.1.1. *Each pair of similar circuits of G is connected in Ω .*

Subproof: It suffices to show for each $3 \leq k \leq n$ that each k -circuit of G is connected in Ω to the unique circuit with vertex set $[k]$ that is similar to C . Let C_0 be a k -circuit such that C, C_0 are connected in Ω , while the sum $\Sigma(C_0)$ of the vertices in C_0 is as small as possible. If $V(C_0) = [k]$ then the required statement holds. Otherwise there is some $\ell \in V(C_0)$ for which $\ell - 1 \notin V(C_0)$. Let C'_0 be obtained by replacing the vertex ℓ with the vertex $\ell - 1$ in C' . Now C'_0 and C_0 form a Θ -graph with a 4-circuit $C'' = [x, \ell - 1, y, \ell]$; since no vertex lies between $\ell - 1$ and ℓ in the ordering, we see that C_4 is a 1243- or 1324-circuit. Thus C'_0 and C_0 are adjacent in Ω ; since $\Sigma C'_0 = \Sigma C_0 - 1$, this contradicts the minimality in the choice of C_0 . \square

Call a circuit C of G *basic* if either $C = [1, 2, 4, 3]$, or if $C = [1, \dots, k]$ for some $k \geq 3$. Note that the first type has $\delta = 0$, while other basic k -circuits have $\delta = k - 2$.

5.1.2. *Each nonspanning circuit of G is connected in Ω to a basic circuit.*

Subproof: For each circuit C of G with canonical ordering v_1, \dots, v_k , call a pair (i, j) *decreasing* in C if $1 \leq i < j \leq k$ and $v_i > v_j$; since the ordering is canonical, any decreasing pair (i, j) satisfies $i > 1$. Similar circuits have the same set of decreasing pairs.

Fix a circuit C of G . Let C' be a circuit connected to C in Ω for which $|V(C')|$ is as small as possible, and, subject to this, C' has as few decreasing pairs as possible. Let C_0 be the unique circuit with vertex set $[k]$ that is similar to C' ; we show that C_0 is basic. Let v_1, v_2, \dots, v_k be the canonical ordering of C_0 , so $v_1 = 1$ and $v_2 < v_k$.

Note that C is connected to C_0 in Ω , and that the decreasing pairs for C_0 are exactly those for C' . If C_0 has no decreasing pair then $C_0 = [1, \dots, k]$ is basic, giving the result. If $k \leq 4$ then C_0 is either one of the basic circuits $[1, 2, 3]$, $[1, 2, 3, 4]$, $[1, 2, 4, 3]$, or $C_0 = [1, 3, 2, 4]$. In the first three cases the result holds, and in the last, the Θ -graph containing circuits C_0 , $[3, 5, 2, 4]$ and $[1, 3, 5, 4]$ certifies that C_0 and $[1, 3, 5, 4]$ are adjacent in Ω , since $[1, 3, 5, 4]$ is similar to $[1, 2, 4, 3]$, the result holds by (5.1.1).

Suppose, therefore, that $k > 4$ and that C_0 has a decreasing pair. Let (a, b) be a decreasing pair for C_0 for which a is as small as possible, and subject to this, b is as large as possible. Note that $a > 1$. Suppose first that $b = a + 1$; since $(a - 1, a + 1)$ is not decreasing we have $v_{a-1} < v_{a+1} < v_a$.

If $a = 2$ then, since $(2, 4)$ is not decreasing we have $v_1 < v_3 < v_2 < v_4$. Let $C_1 = [v_1, v_4, v_5, \dots, v_k]$; note that $|V(C_1)| = k - 2 \geq 3$. Now C_0 and C_1 form a Θ -graph with the 1324-circuit $C_2 = [v_1, v_2, v_3, v_4]$. Therefore C_1 is adjacent to C_0 in Ω and is thus connected to C ; since $|V(C_1)| < |V(C')|$, this contradicts the minimality in the choice of C' .

If $a > 2$ then since $(a - 2, a - 1)$ is not decreasing we have $v_{a-2} < v_{a-1} < v_{a+1} < v_a$. Let $C_1 = [v_1, \dots, v_{a-2}, v_{a+1}, \dots, v_k]$ (noting that $|V(C_1)| = k - 2 \geq 3$). The circuits C_0 and C_1 form a Θ -graph with the 1243-circuit $C_2 = [v_{a-2}, v_{a-1}, v_a, v_{a+1}]$; we obtain a contradiction as in the previous case.

We may thus assume that $b > a + 1$. We will now derive a contradiction by finding a k -circuit C'' , connected to C in Ω , with fewer decreasing pairs than C .

Let $C'_0 = [v'_1, v'_2, \dots, v'_k]$ where $v'_i = v_i$ if $v_i < v_b$, and $v'_i = v_b + 1$ if $v_i \geq v_b$. So C'_0 has vertex set $[k+1] - \{v_b\}$ and is similar to C_0 (and the ordering v'_1, \dots, v'_k is canonical). Let $C''_0 = [v''_1, \dots, v''_k]$, where $v''_i = v'_i$ if $i \neq a$, and $v''_a = v_b$. Again, since $v''_2 \leq v'_2 < v'_k = v''_k$, the ordering v''_1, \dots, v''_k is canonical. The circuits C''_0 and C'_0 form a Θ -graph with the 4-circuit $C_1 = [v'_{a-1}, v_b, v'_{a+1}, v'_a]$. Since $v'_b = v_b + 1 \notin V(C_1)$, the circuit C_1 is similar to $C'_1 = [v'_{a-1}, v'_b, v'_{a+1}, v'_a]$; the fact that $v'_b < v'_a$ and neither $(a-1, b)$ nor $(a, a+1)$ is decreasing in C'_0 gives $v'_{a-1} < v'_b < v'_a < v'_{a+1}$ and so C'_1 and C_1 are 1243-circuits. This implies that C'_0 and C''_0 are adjacent in Ω .

Since $v''_a = v_b < v_b + 1 = v'_b$, the pair (a, b) is not decreasing in C''_0 but is decreasing in C_0 and therefore in C' . We now argue that every decreasing pair (i, j) in C''_0 is decreasing in C'_0 ; this will imply that C''_0 has strictly fewer decreasing pairs than C_0 and therefore fewer such pairs than C' , contradicting the minimality in the choice of C' .

Let (i, j) be a decreasing pair in C''_0 . If $j = a$ then $i < a$ and, using the fact that $(a-1, b)$ is not decreasing in C'_0 , we have $v''_i = v'_i \leq v'_{a-1} < v'_b = v_b + 1 = v''_a + 1$ and so $v''_i \leq v''_a = v'_j$, contradicting the definition of (i, j) . If $j \neq a$ then, using the fact that $v'_\ell \geq v''_\ell$ for all ℓ , we have $v'_i \geq v''_i > v''_j = v'_j$ so (i, j) is decreasing in C'_0 . This fact establishes the aforementioned contradiction. \square

5.1.3. *If C_1, C_2 are connected in Ω , then $\delta(C_1) = \delta(C_2)$.*

Subproof: It suffices to show this for C_1, C_2 adjacent in Ω . Let H be a Θ -graph of Ω containing C_1, C_2 and a 1324-circuit or 1243-circuit C_3 ; note that $\delta(C_3) = 0$. Let x, y be the vertices of H for which $E(H)$ is the disjoint union of three xy -paths P_1, P_2, P_3 , where each C_i has edge set disjoint from that of P_i . For each xy -path P with vertices $x = x_0, x_1, \dots, x_\ell = y$ listed in path order, let

$$\delta(P) = |\{i \in [\ell] : x_i > x_{i-1}\}| - |\{i \in [\ell] : x_i < x_{i-1}\}|.$$

By construction, we have

$$(\delta(C_1), \delta(C_2), \delta(C_3)) = (|\delta(P_3) - \delta(P_2)|, |\delta(P_1) - \delta(P_3)|, |\delta(P_2) - \delta(P_1)|).$$

Since $\delta(C_3) = 0$, this gives $\delta(P_1) = \delta(P_2)$ and so $\delta(C_1) = \delta(C_2)$. \square

By (5.1.3), vertices of Ω in the same component have the same value of δ . By (5.1.2), any two vertices C_1, C_2 of Ω with $\delta(C_1) = \delta(C_2) = d$ are both connected to the unique basic circuit C_0 with $\delta(C_0) = d$, so are connected in Ω ; the lemma follows. \square

Lemma 5.2. *Let $n \geq 5$ and let (G, \mathcal{B}, \leq) be an ordered biased graph with $G \cong K_n$. If all 1324-circuits and 1243-circuits of G are balanced, then there exists $a > 0$ such that $(G, \mathcal{B}) - v \cong K_{n-1}^a$ for each $v \in V$.*

Proof. We may assume that (G, \leq) is K_n with the usual ordering. Let $\Omega = \Omega(G)$. Note that a nonspanning circuit C of G must have $\delta(C) \leq |V(C)| - 2 \leq n - 3$. For each $0 \leq d \leq n - 3$, let \mathcal{U}_d be the set of nonspanning circuits C of G with $\delta(C) = d$. Note that each \mathcal{U}_d is nonempty; by Lemma 5.1, the \mathcal{U}_d are the components of Ω .

By the definition of Ω , hypothesis, and the Θ -property, adjacent vertices of Ω are either both balanced or both unbalanced; it follows that for each $d \geq 0$, we either have $\mathcal{U}_d \subseteq \mathcal{B}$ or $\mathcal{U}_d \cap \mathcal{B} = \emptyset$. Let \mathcal{D} be the set of all $0 \leq d \leq n - 3$ for which $\mathcal{U}_d \subseteq \mathcal{B}$. Since $\mathcal{U}_0 \cap \mathcal{B}$ contains the circuit $[1, 2, 4, 3]$, we have $0 \in \mathcal{D}$.

Let $d_1, d_2 \in \mathcal{D}$ with $0 < d_1 < d_2$; we show that $d_2 - d_1 \in \mathcal{D}$. Let H be the Θ -graph containing $C_2 = [1, 2, \dots, d_2 + 2]$ together with a single edge from 1 to $d_1 + 2$. We have $\delta(C_2) = d_2$, and the other two circuits C_0, C_1 of H satisfy $\delta(C_0) = d_2 - d_1$ and $\delta(C_1) = d_1$. Since $d_1, d_2 \in \mathcal{D}$ we have $\{C_1, C_2\} \subseteq \mathcal{B}$ and so $C_0 \subseteq \mathcal{B} \cap \mathcal{U}_{d_2-d_1}$ by the Θ -property. This implies that $\mathcal{U}_{d_2-d_1} \subseteq \mathcal{B}$ and so $d_2 - d_1 \in \mathcal{D}$. Thus \mathcal{D} contains zero and is closed under absolute differences. It follows that there exists $a > 0$ for which $\mathcal{D} = \{d \in \{0, \dots, n - 3\} : a|d\}$, and thus a nonspanning circuit C of G is balanced if and only if $a|\delta(C)$. Thus $(G, \mathcal{B}) - v \cong K^a(n - 1)$ for each vertex v of G . \square

Combining this Lemma with Theorem 4.1 and Lemma 2.1, we get the following restatement of our main theorem with explicit bounds in terms of Ramsey numbers and our stronger, ‘ordered’ isomorphism.

Theorem 5.3. *Let $r, s \geq 1$, $t \geq 4$ and $n \geq R_4(3r!, 3s!, 5, t + 1)$. If (G, \mathcal{B}, \leq) is an ordered biased graph with $G \cong K_n$, then (G, \mathcal{B}, \leq) has an ordered biased subgraph isomorphic to K_r^u , K_s^o , or K_t^a for some $a > 0$.*

Finally, we give a construction implying that the vertex deletion in Lemma 5.2 is necessary, in fact showing that there are doubly exponentially many counterexamples if the vertex deletion is not insisted upon. Since Lemma 5.2 controls the behaviour of every nonspanning cycle, all the wildness occurs within the Hamilton cycles.

Theorem 5.4. *For all $n \geq 10$, there are at least $2^{2^{n-9}}$ ordered biased graphs (G, \mathcal{B}, \leq) where $G = K_n$ and every 1243-circuit and 1324-circuit of G is balanced, but (G, \mathcal{B}) does not arise from any group-labelling.*

Proof. Let \mathcal{P} be the collection of ordered partitions (I, J) of $[n]$ for which $\{1, 2, n - 2, n - 1\} \subseteq I$ and $\{3, 4, 5, n\} \subseteq J$, so $|\mathcal{P}| = 2^{n-8}$. Let $G = (V, E) = K_n$ and let $(G, \mathcal{B}, \leq) = K^a(n)$ where $a = n - 4$. For each $(I, J) \in \mathcal{P}$, let $C_{I,J}$ be the n -circuit with canonical ordering

$u_1, \dots, u_\ell, v_1, \dots, v_k$, where the u_i are the elements of I in increasing order and the v_j are the elements of J in increasing order. Note that $\delta(C_{I,J}) = n - 4$ and therefore that $C_{I,J} \in \mathcal{B}'$ for each (I, J) , and that the map $(I, J) \mapsto C_{I,J}$ is injective. For each $\mathcal{Q} \subseteq \mathcal{P}$, let $\mathcal{B}_{\mathcal{Q}} = \mathcal{B} - \{C_{I,J} : (I, J) \in \mathcal{Q}\}$.

5.4.1. $(G, \mathcal{B}_{\mathcal{Q}})$ is a biased graph for all $\mathcal{Q} \subseteq \mathcal{P}$.

Subproof: If not, then there is a Θ -subgraph H containing exactly two balanced circuits. No such subgraph exists in (G, \mathcal{B}) , so it must be the case that H contains an unbalanced n -circuit $C_0 = C_{I,J}$ for some $(I, J) \in \mathcal{Q}$ and two other balanced circuits C_1, C_2 ; since no Θ -subgraph of K_n contains two n -circuits, we must have $C_1, C_2 \in \mathcal{B}$. Let P_0, P_1, P_2 be the three xy -paths so that $E(C_i) = E(H) - E(P_i)$ for each $i \in \{0, 1, 2\}$. Since C_0 is an n -circuit, the path P_0 is a chord of C_0 , so $|E(P_0)| = 1$ and $|E(P_1)| + |E(P_2)| = n$. As in the proof of (5.1.3), let

$$\delta(P) = |\{i \in [\ell] : x_i > x_{i-1}\}| - |\{i \in [\ell] : x_i < x_{i-1}\}|$$

for each path P with vertices occuring in order $x_i : i \in [\ell]$; so

$$(\delta(C_0), \delta(C_1), \delta(C_2)) = (|\delta(P_2) - \delta(P_1)|, |\delta(P_1) - \delta(P_0)|, |\delta(P_0) - \delta(P_2)|).$$

We have $\delta(C_0) = n - 4$ and, since $C_1, C_2 \in \mathcal{B}$, both $\delta(C_1)$ and $\delta(C_2)$ are multiples of $n - 4$. The above also gives $n - 4 = \delta(C_0) = \pm\delta(C_1) \pm \delta(C_2)$. Moreover, since $\delta(C_i) \leq |V(C_i)| - 2 \leq n - 3$ for each $i \in \{1, 2\}$, we must have $\{\delta(C_1), \delta(C_2)\} = \{0, n - 4\}$; we may assume that $\delta(C_1) = 0$ (so $\delta(P_2) = \delta(P_0)$) and $\delta(C_2) = n - 4$. Thus $n - 4 = \delta(C_2) \leq |V(C_2)| - 2$ and so $|V(C_2)| \geq n - 2$; since $\delta(C_2) \equiv |V(C_2)| \pmod{2}$ and $|V(C_2)| < n$ we must have $|V(C_2)| = n - 2$. Thus $|E(P_2)| = |E(H)| - |E(C_2)| = (n + 1) - (n - 2) = 3$ and so C_1 is a 4-cycle with $\delta(C_1) = 0$.

However, the construction of \mathcal{P} and $C_0 = C_{I,J}$ gives that $C_0 = [u_1, u_2, \dots, u_\ell, v_1, v_2, \dots, v_k]$ where the u_i occur in increasing order with $(u_1, u_2, u_{\ell-1}, u_\ell) = (1, 2, n - 2, n - 1)$ and the v_i occur in increasing order with $(v_1, v_2, v_3, v_k) = (3, 4, 5, n)$. The 4-cycle C_1 satisfies $C_1 = [x_1, x_2, x_3, x_4]$ where x_1, x_2, x_3, x_4 are consecutive vertices in C . Any such quadruple of consecutive vertices is either a subsequence of (u_1, \dots, u_ℓ) or (v_1, \dots, v_k) , or is one of the quadruples $(n - 2, n - 1, 3, 4)$, $(n - 1, 3, 4, 5)$, $(v_{k-1}, n, 1, 2)$ or $(n, 1, 2, u_3)$. All these possibilities imply that $\delta(C_1) = 2$, a contradiction. \square

We now prove that if $\emptyset \neq \mathcal{Q} \neq \mathcal{P}$, then $(G, \mathcal{B}_{\mathcal{Q}})$ does not arise from a group-labelling. Since there are $2^{|\mathcal{P}|} - 2 > 2^{2^{n-9}}$ possible \mathcal{Q} , this will give the lemma. Suppose it does; let Γ be a group with identity id , and let $\gamma : E \rightarrow \Gamma$ be a labelling of the edges of some orientation \vec{G} of G whose balanced circuits are precisely those in $\mathcal{B}_{\mathcal{Q}}$. It is well-known and

routine to show that applying either of the following two operations to a Γ -labelling of an orientation of a graph do not change the associated biased graph:

- (a) reversing the orientation of an edge, and replacing the label of that edge with its inverse in Γ ;
- (b) for a vertex v and a constant $\alpha \in \Gamma$, right-multiplying the label of every edge oriented towards v by α , then left-multiplying the label of every edge oriented away from v by α^{-1} .

By possibly applying (a) repeatedly, we may assume that every edge ij with $i < j$ is oriented towards j . By then applying (b) with $\alpha = \gamma(1v)^{-1}$ to each vertex $v \in \{2, \dots, n\}$, we may assume that $\gamma(1i) = \text{id}$ for each $i > 1$. Let $1 < p < q < r \leq n$. Then the 1243-circuit $C = [1, p, r, q]$ is balanced and so $\text{id} = \gamma(1p)\gamma(pr)\gamma(qr)^{-1}\gamma(1q)^{-1} = \gamma(pr)\gamma(qr)^{-1}$, giving $\gamma(pr) = \gamma(qr)$. Similarly, the 1324-circuit $C' = [1, q, p, r]$ is balanced, so $\text{id} = \gamma(1q)\gamma(pq)^{-1}\gamma(pr)\gamma(r1)^{-1}$, giving $\gamma(pq) = \gamma(pr) = \gamma(qr)$. It follows that every pair of incident edges of $G - \{1\}$ are assigned the same label by Γ ; connectedness gives that all edges of $G - 1$ are assigned the same label $g \in \Gamma$. Let γ_0 by the labelling obtained from γ by applying (b) with $v = 1$ and $\alpha = g$; now γ_0 assigns a label of g to every edge of G , and $\mathcal{B}_{\mathcal{Q}}$ is the set of balanced cycles of (γ', \vec{G}) by construction. But γ_0 is also a labelling of edges of G with elements of the cyclic subgroup Γ_0 of Γ generated by g . By the definition of $K^a(n)$ we therefore have $(G, \mathcal{B}_{\mathcal{Q}}, \leq) = K^a(n)$, where $a = 0$ if Γ_0 is infinite, and $a = |\Gamma_0|$ otherwise. This implies that a circuit C of G is in $\mathcal{B}_{\mathcal{Q}}$ if and only if $a|\delta(C)$. However, if $(I, J) \in \mathcal{Q}$ and $(I', J') \in \mathcal{P} - \mathcal{Q}$, we have $C_{I,J} \notin \mathcal{B}_{\mathcal{Q}}$ and $C_{I',J'} \in \mathcal{B}_{\mathcal{Q}}$ while $\delta(C_{I,J}) = \delta(C_{I',J'}) = n - 4$, a contradiction. \square

6. REFERENCES

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